**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Response Under 37 CFR 1.116

**EXPEDITED PROCEDURE** 

Toshio YAMADA

Customer Number: 20277

Serial No.: 09/779,751

Confirmation Number: 5852

Group Art Unit: 2127

Filed: February 09, 2001

Examiner: Lilian VO

For:

SEMICONDUCTOR INTEGRATED CIRCUIT, COMPUTER SYSTEM, DATA

PROCESSOR AND DATA PROCESSING METHOD

## AMENDMENT UNDER 37 C.F.R. § 1.116

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 RECEIVED

APR 2 7 2004

**Technology Center 2100** 

Sir:

Exer 5/27/04

In response to the Office Action dated January 29, 2004, having a shortened statutory period for response set to expire April 29, 2004, please amend the above-identified application as follows: